



IPW

Docket No.: INTEL-021

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of  
Edward A. BURTON, Kumar  
ANSHUMALI

Assignee: **INTEL CORPORATION**

Serial No.: 10/607,550

Group Art Unit: 2818

Filed: 6/27/2003

Confirmation No.: 7414

Examiner: Long K. Tran

Customer No.: 34610

For: **ADDITION OF METAL LAYERS WITH SIGNAL REALLOCATION TO A MICROPROCESSOR FOR INCREASED FREQUENCY AND LOWER POWER**

**RESPONSE TO RESTRICTION REQUIREMENT**

U.S. Patent and Trademark Office  
220 20th St. South  
Customer Window, Mail Stop: Amendment  
Crystal Plaza Two, Lobby, Room 1B03  
Arlington, VA 22202

Sir:

In response to the Restriction Requirement mailed on November 30, 2004 Applicant elects Group I (claims 1-14, 20 and 30) without traverse for further prosecution on the merits.

Applicants respectfully submit that this application is in condition for allowance.

Favorable consideration and prompt allowance of the application is respectfully requested.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this,

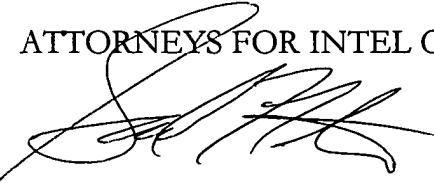
Serial No. 10/607,550

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concurrent and future replies, including extension of time fees, to Deposit Account 16-0607 and  
please credit any excess fees to such deposit account.

Respectfully submitted,  
FLESHNER & KIM, LLP

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**Date: December 6, 2004**

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